

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 10-24-2003  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: FPGA Lookup Table with Transmission  
Gate Structure for Reliable Low-  
Voltage Operation  
Attorney Docket Number:: X-1128-1D US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 04  
Small Entity?:: No  
Petition included?:: No

### APPLICANT INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: P.R. China  
Status:: Full Capacity  
Given Name:: Tao  
Family Name:: Pi  
Street:: 1055 Escalon Avenue #507  
City:: Sunnyvale  
State or Province:: California  
Postal or Zip Code:: 94085

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Patrick  
Middle Name:: J.  
Family Name:: Crotty  
Street:: 3255 Woodmere Drive  
City:: San Jose

State or Province:: California  
Postal or Zip Code:: 95136

### **CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

### **REPRESENTATIVE INFORMATION**

Representative Customer Number::	24309	
----------------------------------	-------	--

### **DOMESTIC PRIORITY INFORMATION**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/241,094	09-10-02

### **ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124